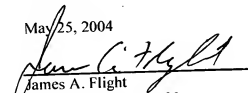




17w  
PATENT  
Docket No. INTEL/16136X

**IN THE UNITED STATES PATENT  
AND TRADEMARK OFFICE**

Applicants: Sun	)	I hereby certify that this paper is
	)	being deposited with the United
Serial No.: 10/608,683	)	States Postal Service with
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Filed: June 27, 2003	)	mail in an envelope addressed to:
	)	Commissioner for Patents, P.O.
Assignee: Intel Corporation	)	Box 1450, Alexandria, VA 22313-
	)	1450 on this date:
For: METHODS AND APPARATUS TO	)	
MANAGE A CACHE MEMORY	)	May 25, 2004
	)	
Group Art Unit: 2171	)	
	)	James A. Flight
Examiner: Unknown	)	Registration No. 37,622
	)	Attorney for Applicant(s)

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

The patents and/or publications listed on the enclosed PTO Form-1449 are submitted pursuant to 37 CFR §§ 1.56, 1.97, and 1.98. Copies of the patents or publications are enclosed.

**TIME OF FILING**

This information disclosure statement is being filed to the best of the undersigned's knowledge, before the mailing date of a first Office action on the merits. In accordance with 37 CFR §1.97(b), no certification or fee is required.



## PRIOR AND RELATED APPLICATIONS

The examiner is advised of the following related applications:

Programs	Applicant(s):	Sun
	Serial No.	10/424,356
	Filing Date:	April 28, 2003
	Title:	Methods and Apparatus to Detect Patterns in
	Status:	Pending
	Applicant(s):	Sun
	Serial No.	10/608,324
	Filing Date:	June 27, 2003
Objects	Title:	Methods and Apparatus to Prefetch Memory
	Status:	Pending
	Applicant(s):	Sun
	Serial No.	10/833,762
	Filing Date:	April 28, 2004
	Title:	Methods and Apparatus to Detect a Macroscopic
Transaction Boundary in a Program	Status:	Pending

# METHOD OF PAYMENT

☒ No fee is required.

☐ Attached is a check in the amount of \$

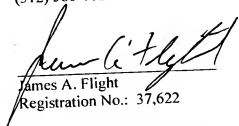
The Commissioner is authorized to charge any fee deficiency required by this paper, or credit any overpayment, to Deposit Account No. 50-2455. A copy of this paper is enclosed.

Correspondence Address:

Respectfully submitted,

GROSSMAN & FLIGHT, LLC.  
20 N. Wacker Drive  
Suite 4220  
Chicago, Illinois 60606  
(312) 580-1020

By:

  
James A. Flight  
Registration No.: 37,622

Attorneys for Intel Corporation

May 25, 2004

Form PTO-1449 (Modified)

U.S. Department of Commerce  
Patent and Trademark Office

Atty. Docket No.

INTEL/16136X

Serial No.

10/608,683

Applicant

Sun

Filing Date

6/27/2003

Group Art Unit

2171

## INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

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C05	S. Dhodapkar et al., "Comparing Program Phase Detection Techniques", Micro-2003, 2003.
C06	V. Bala et al., "Dynamo: A transparent dynamic optimization system", PLDI '2000, June 2000.
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C10	T. Sherwood et al., "Phase Tracking and Prediction", ISCA-2003, June 2003.
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C12	C.E. Shannon, "A Mathematical Theory of Communication", Bell Syst. Tech. J., 27, 379-423, 623-656, July and October, 1948.
C13	Standard Performance Evaluation Corporation (SPEC) JBB2000 [online]. SPEC [retrieved on May 11, 2004]: Retrieved from the Internet: <URL: <a href="http://www.spec.org/jbb2000/">http://www.spec.org/jbb2000/</a> >, 2 pages
C14	Standard Performance Evaluation Corporation (SPEC) JBB2000 [online]. SPEC AppServer [retrieved on May 14, 2004]: Retrieved from the Internet: <URL: <a href="http://www.spec.org/jAppServer2002/">http://www.spec.org/jAppServer2002/</a> >, 2 pages
C15	Dinero IV Trace-Driven Uniprocessor Cache Simulator: [retrieved on May 14, 2004]: Retrieved from the Internet: <URL: <a href="http://www.cs.wisc.edu/~markhill/DineroIV/">http://www.cs.wisc.edu/~markhill/DineroIV/</a> >, 2 pages

EXAMINER

DATE CONSIDERED

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

